```
        (FILE 'USPAT' ENTERED AT 08:00:21 ON 20 NOV 1998)
L1        115 S DATA(W)CAPTURE(4A)SYSTEM
L2        115 S (DATA(W)CAPTURE)(4A)SYSTEM
L3         19 S ((DATA(W)CAPTURE)(4A)SYSTEM)(P)GENERAT###
L4      14710 S LATCH####(4W)DATA
L5          0 S L3 AND L4
L6      10215 S DELAY(2W)SIGNAL
L7         69 S LATCH###(W)DATA(W)SIGNAL
L8       1838 S (GENERAT###(5A)DATA)(2P)((DATA)(4A)(COMPARATOR)))
L9          2 S L7 AND L8
L10         0 S L7 AND L8 AND L6
L11         6 S L6 AND L7
L12         0 S L11 AND L8

=> d 1-6 111
```

1. 5,822,330, Oct. 13, 1998, Method and system for dynamically adjusting
signal skewing; Patrick Allen Buckland, 371/1; 375/254; 395/558, 559
[IMAGE AVAILABLE]

2. 5,598,176, Jan. 28, 1997, Time period adjustable bar graph display;
Eric Klingenfus, 345/35, 42 [IMAGE AVAILABLE]

3. 5,587,950, Dec. 24, 1996, Test circuit in clock synchronous
semiconductor memory device; Seiji Sawada, et al., 365/201, 233 [IMAGE
AVAILABLE]

4. 5,511,029, Apr. 23, 1996, Test circuit in clock synchronous
semiconductor memory device; Seiji Sawada, et al., 365/201, 233 [IMAGE
AVAILABLE]

5. 5,260,903, Nov. 9, 1993, Semiconductor memory device; Junichi Suyama,
et al., 365/189.05, 230.08, 233 [IMAGE AVAILABLE]

6. 5,159,217, Oct. 27, 1992, Brownout and power-up reset signal
generator; Gordon L. Mortensen, et al., 327/143, 286, 393; 361/92 [IMAGE
AVAILABLE]